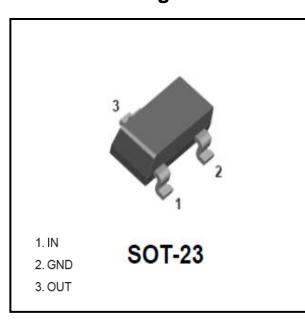




# **Digital Transistors (Built-in Resistors)**



#### **Features**

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion
- NPN

### **Mechanical Data**

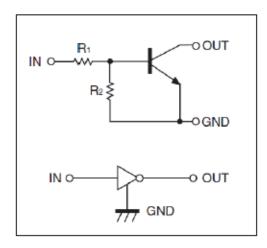
• Package: SOT-23

• Terminals: Tin plated leads, solderable per

J-STD-002 and JESD22-B102

• Marking: 134

#### **■**Equivalent circuit



#### ■Maximum Ratings (Ta=25°C Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V <sub>cc</sub>	V		50
Input Voltage	V <sub>IN</sub>	V		-5 to +12
Output Current	Io	mA		500
Power Dissipation	$P_{D}$	mW		200
Junction Temperature	Tj	$^{\circ}$		150
Storage Temperature	T <sub>STG</sub>	$^{\circ}$		-55 to +150



### ■Electrical Characteristics (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input valtage	$VI_{(off)}$	V	V <sub>CC</sub> =5V,Ic=100uA	0.3		
Input voltage	VI <sub>(on)</sub>	V	V <sub>0</sub> =0.3V,Ic=20mA			2
Output voltage	Vo <sub>(on)</sub>	V	Io /Ii =10mA/2.5mA			0.3
Input current	lı	mA	V <sub>I</sub> =5V			3.6
Output current	I <sub>O(off)</sub>	uA	V <sub>CC</sub> =50V, Vi=0			0.5
DC current gain	Gı		Vo=5V, Io =50mA	56		
Input resistance	R <sub>1</sub>	kΩ		1.54	2.2	2.86
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>		3.6		4.5	5.5
Transition frequency	f <sub>T</sub>	MHz	V <sub>CE</sub> =10V,I <sub>E</sub> =5mA, f=100MHz		200	

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
DDTC123YCA	F2	Approximate 0.009	3000	30000	120000	7" reel



#### **■ Characteristics** (Typical)

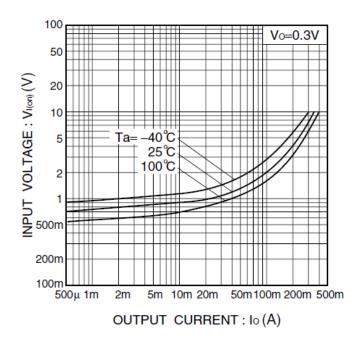


Fig.1 Input voltage vs. output current (ON characteristics)

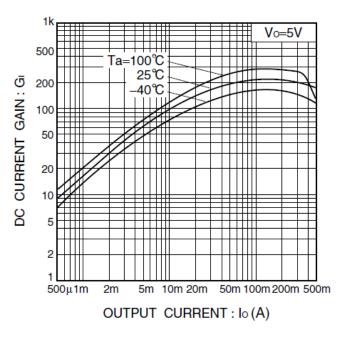


Fig.3 DC current gain vs. output current

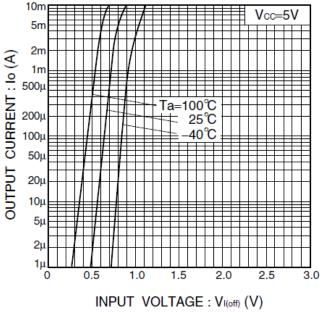


Fig.2 Output current vs. input voltage (OFF characteristics)

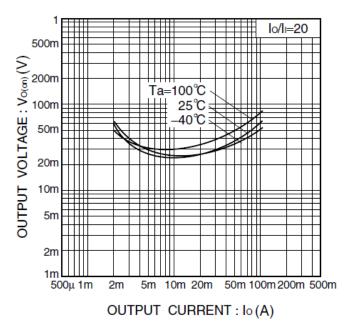
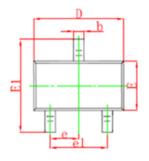
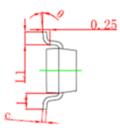


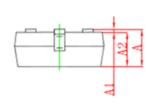
Fig.4 Output voltage vs. output current



## **■SOT-23 Package Outline Dimensions**

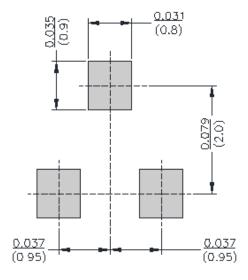






	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol Symbol	Min	Max	Min	Max	
A	0. 900	1. 150	0. 035	0. 045	
A1	0.000	0. 100	0.000	0.004	
A2	0. 900	1. 050	0. 035	0.041	
b	0. 300	0. 500	0.012	0.020	
c	0. 100	0. 200	0.004	0.008	
D	2. 800	3, 000	0. 110	0. 118	
Е	1. 200	1. 400	0.047	0. 055	
E1	2. 250	2. 550	0.089	0.100	
e	0. 950TYP		0. 037TYP		
el	1.800	2. 000	0.071	0.079	
L	0. 550REF		0. 022REF		
L1	0. 300	0. 500	0.012	0.020	
θ	0°	8°	0°	8°	

## ■SOT-23 Suggested Pad Layout





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